

FIG. 1

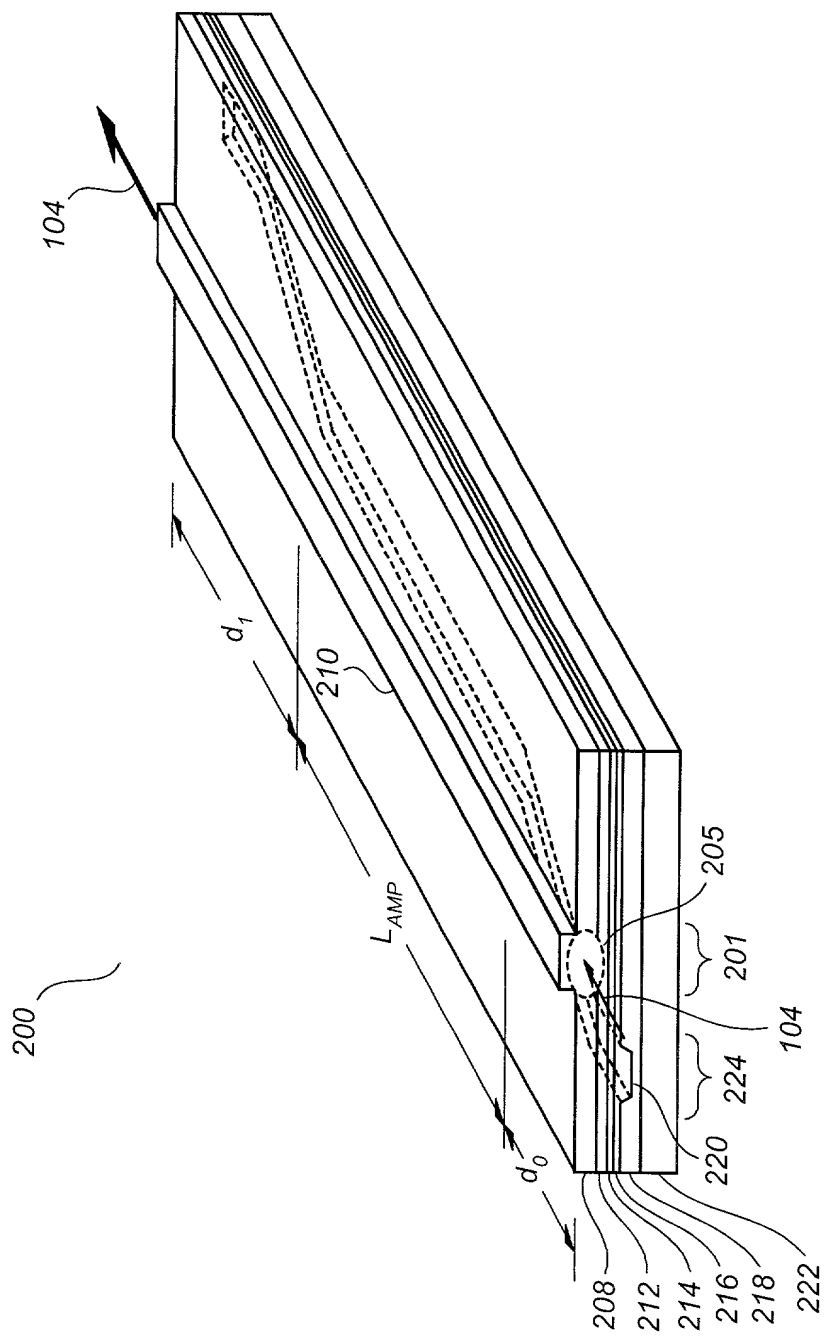


FIG. 2

FIG. 3

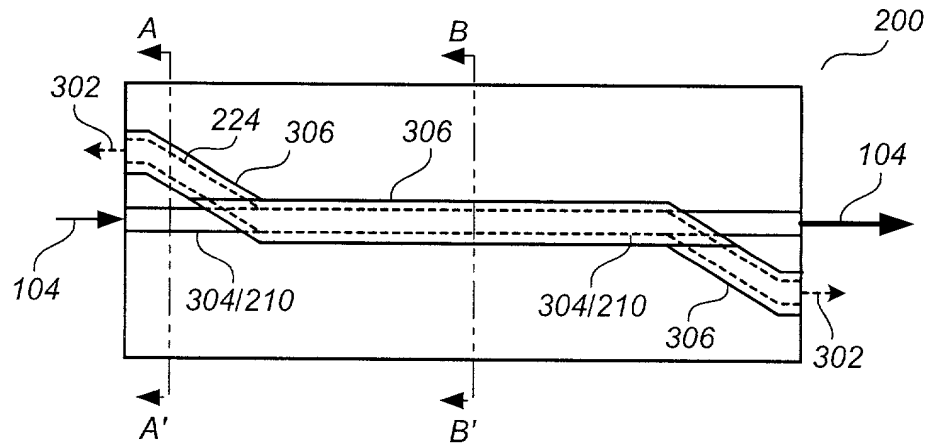


FIG. 4

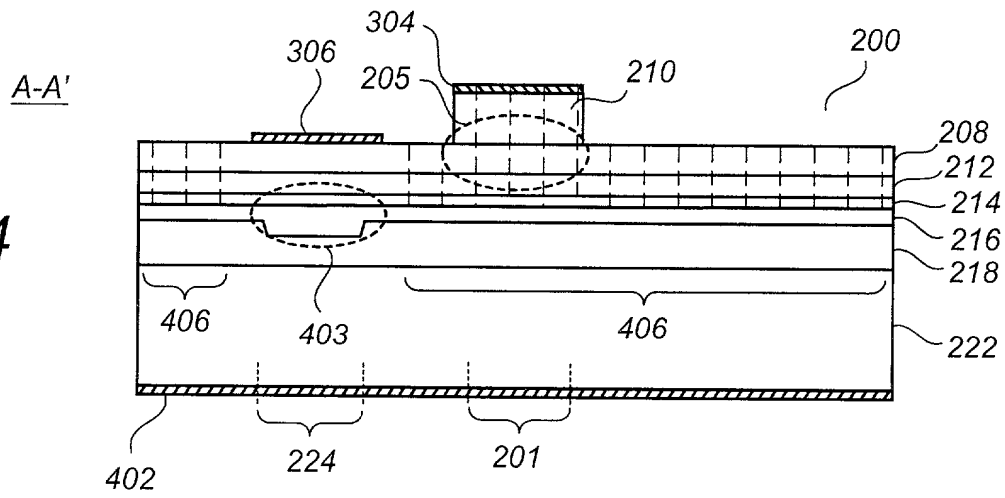
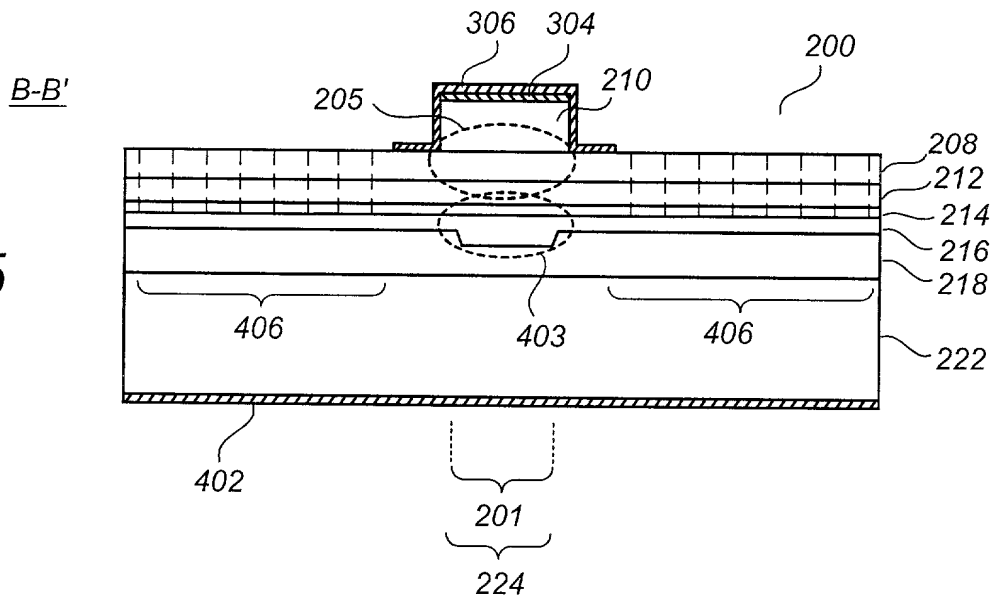


FIG. 5



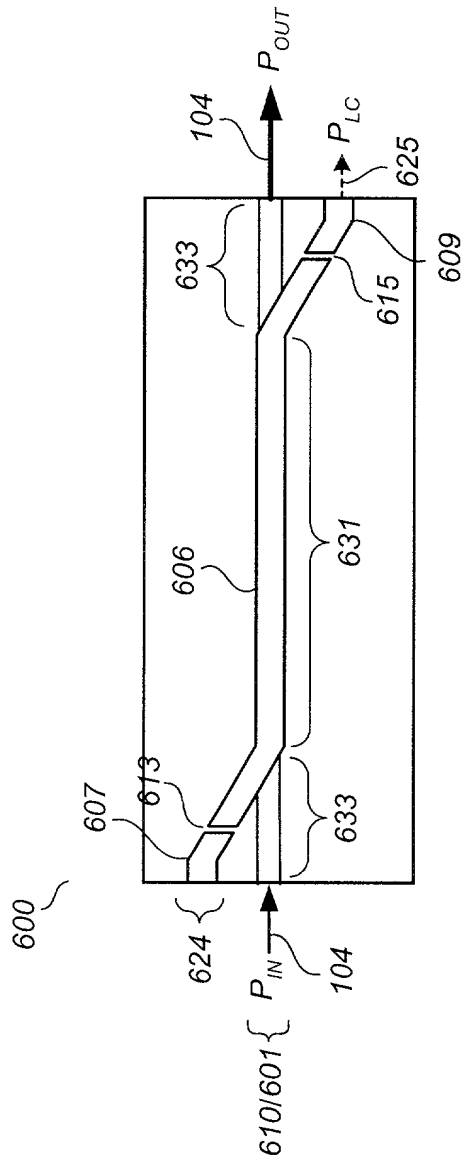


FIG. 6

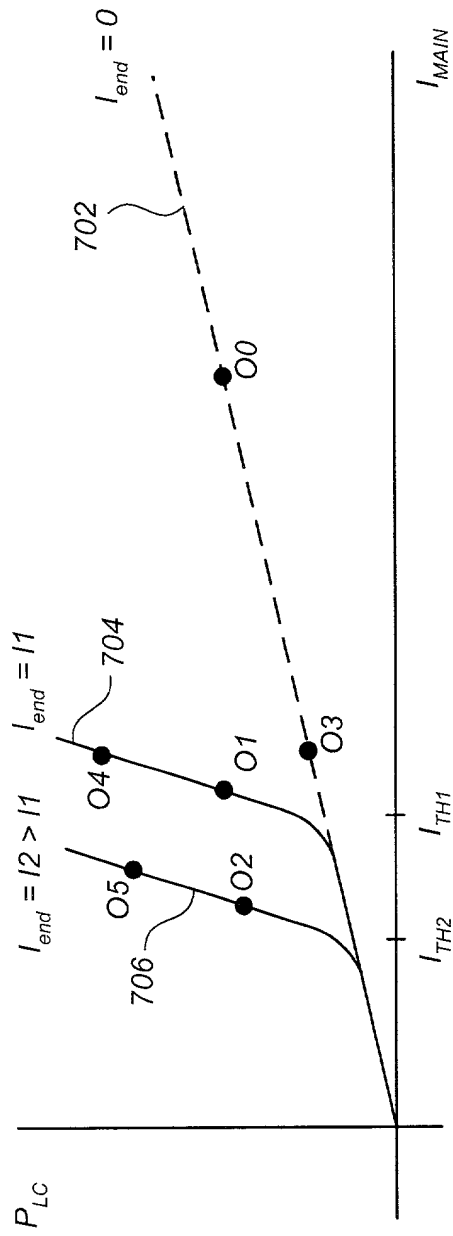


FIG. 7

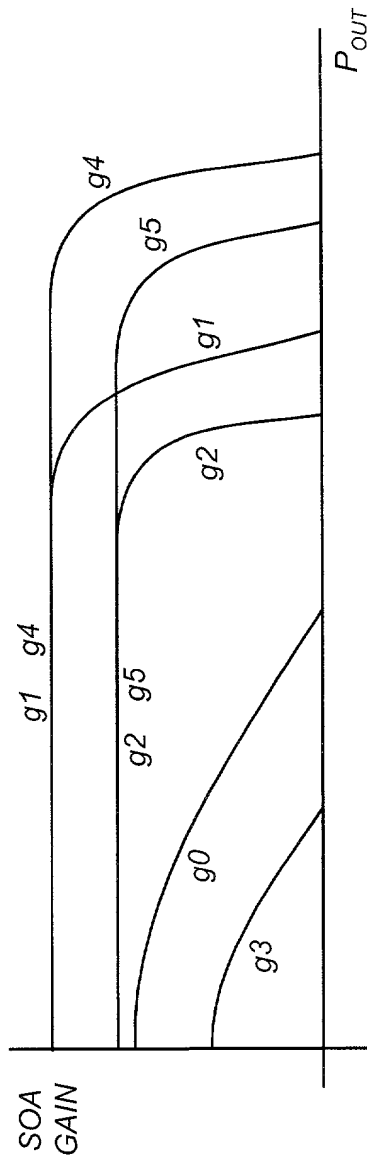


FIG. 8A

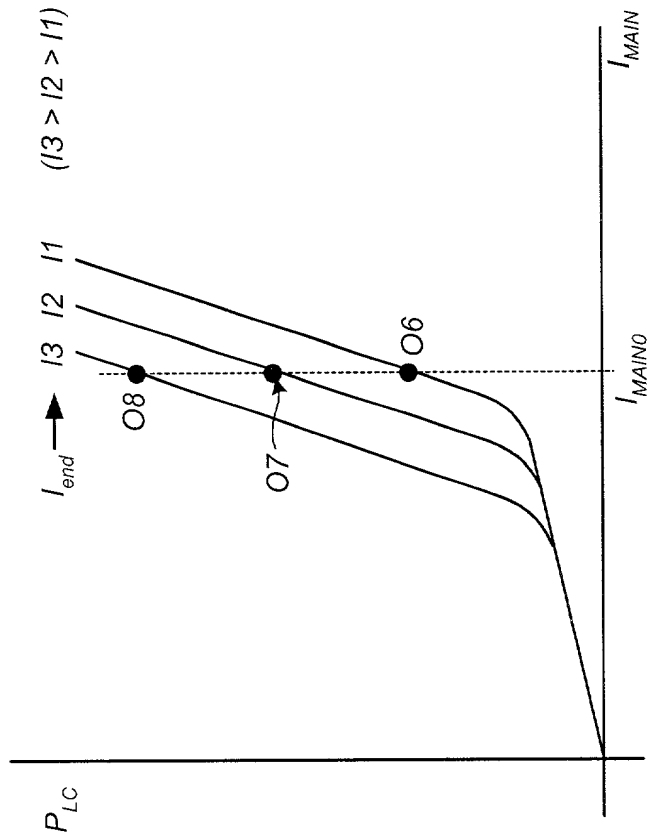


FIG. 8B

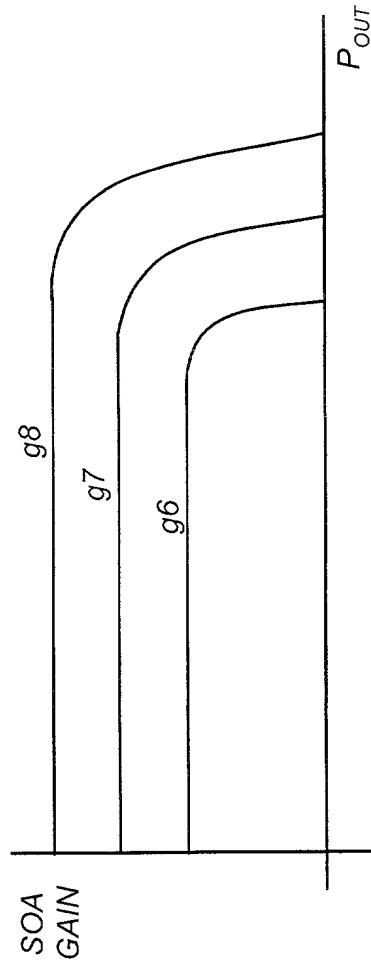


FIG. 8C

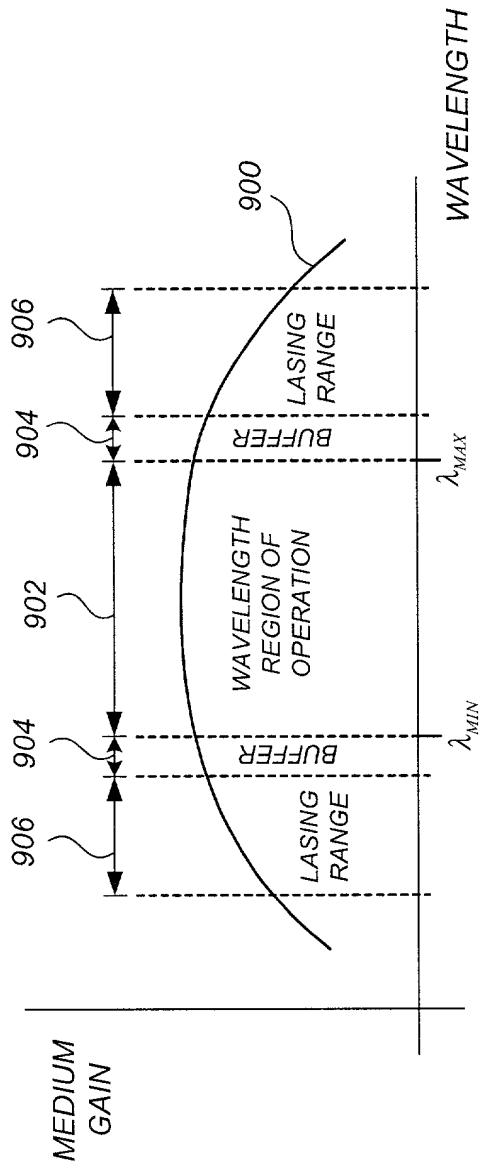


FIG. 9

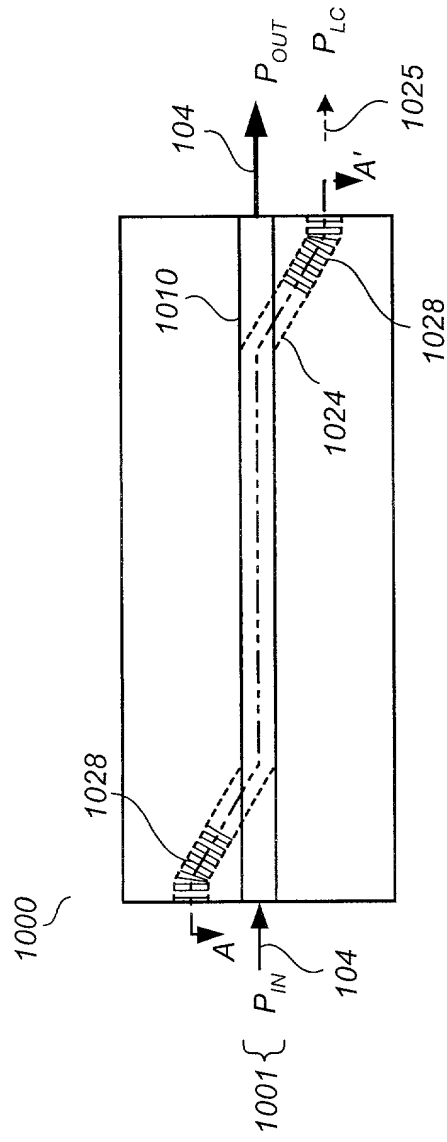


FIG. 10

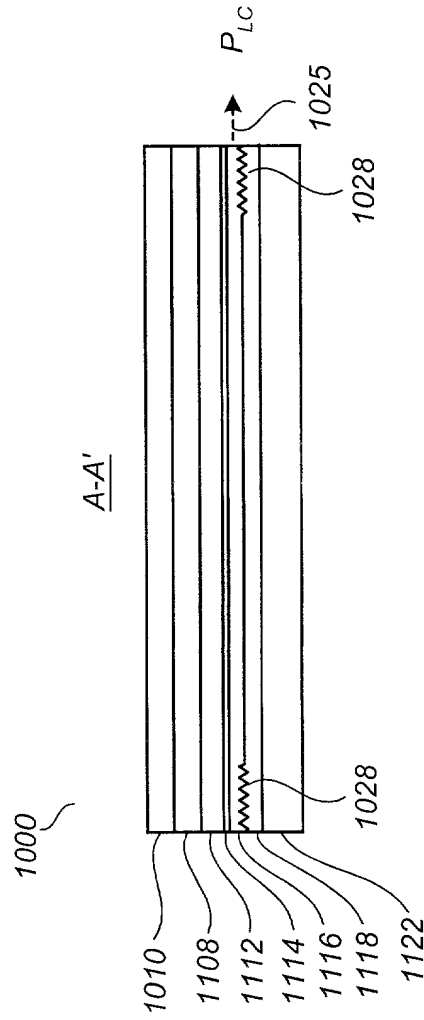


FIG. 11



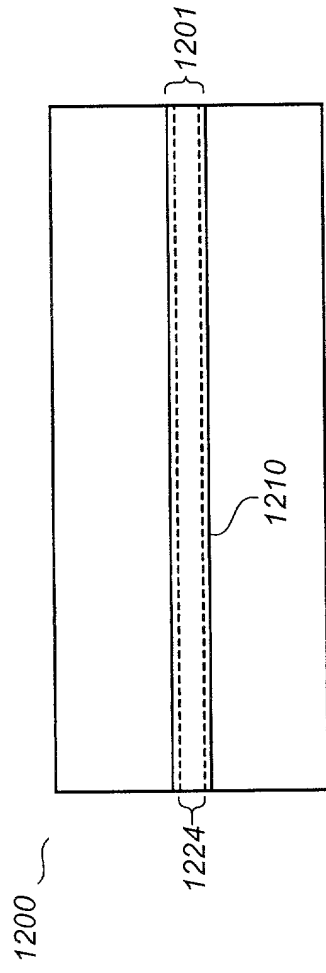


FIG. 12

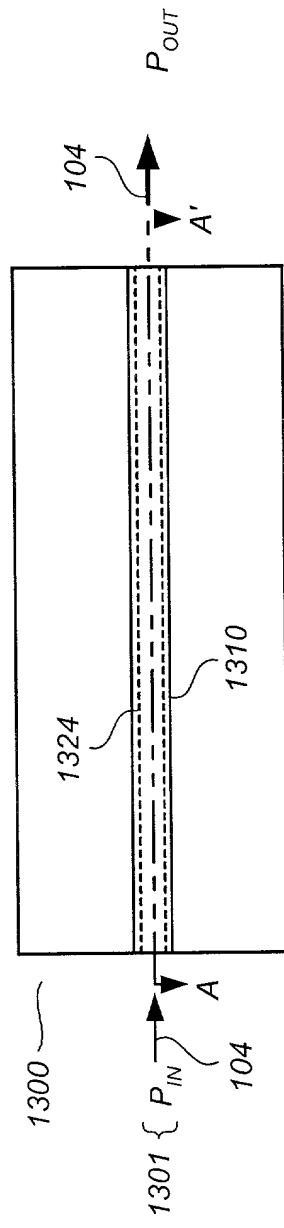


FIG. 13

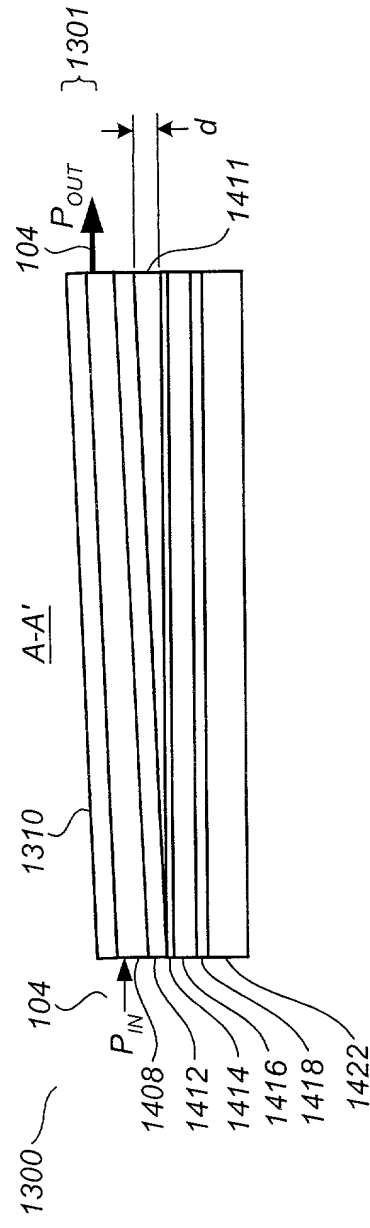
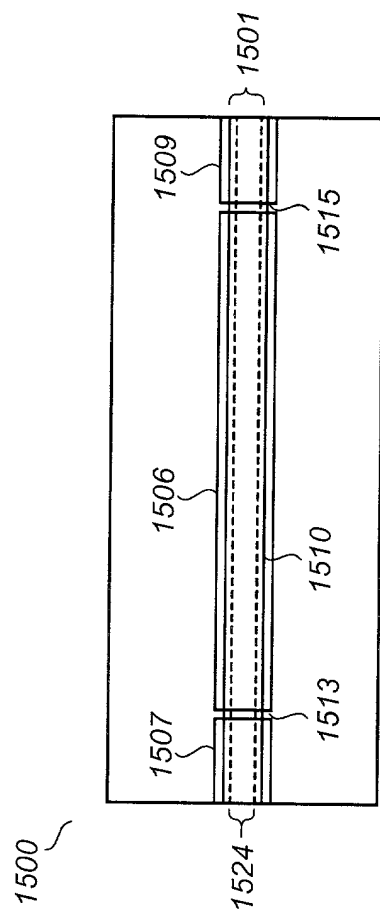


FIG. 14

FIG. 15



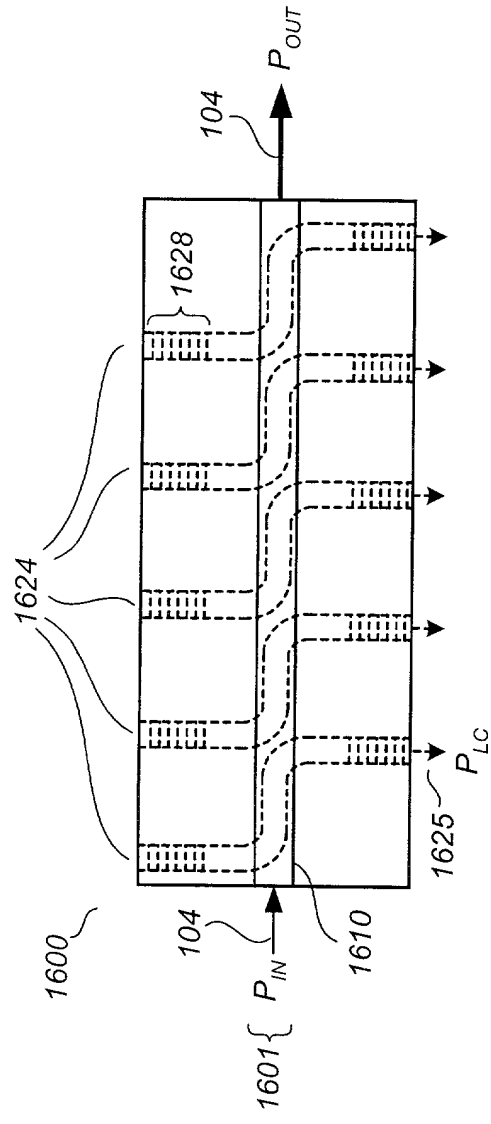


FIG. 16

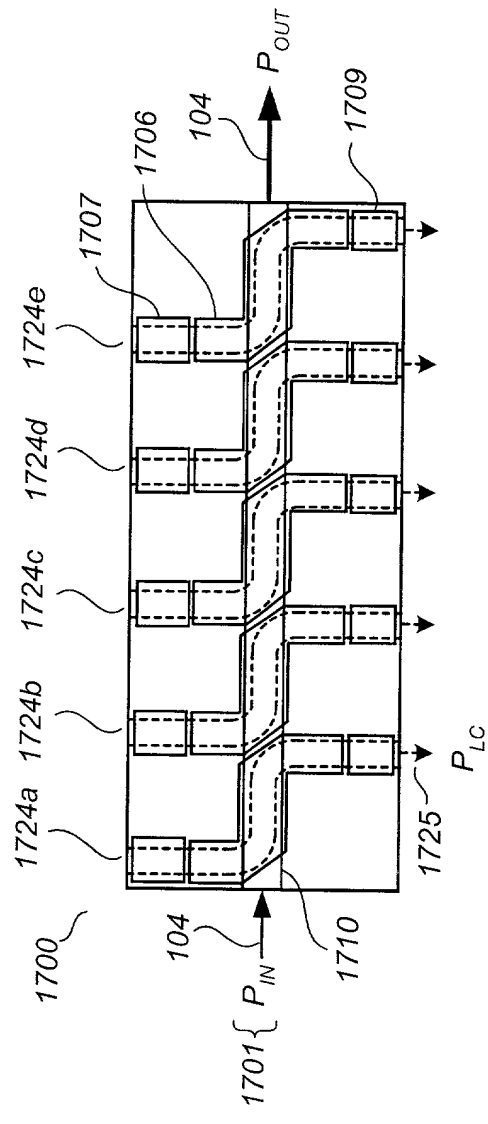


FIG. 17

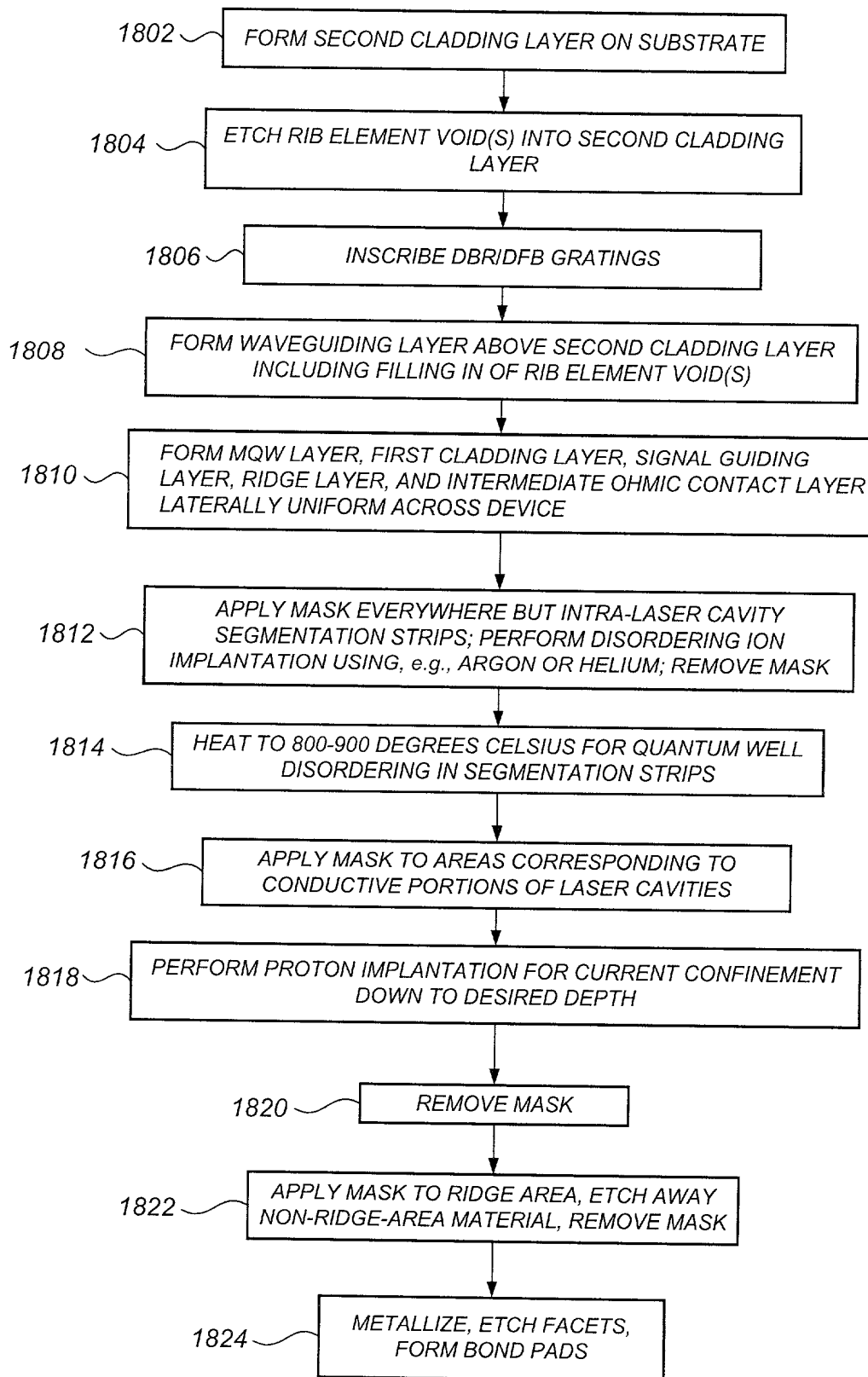


FIG. 18

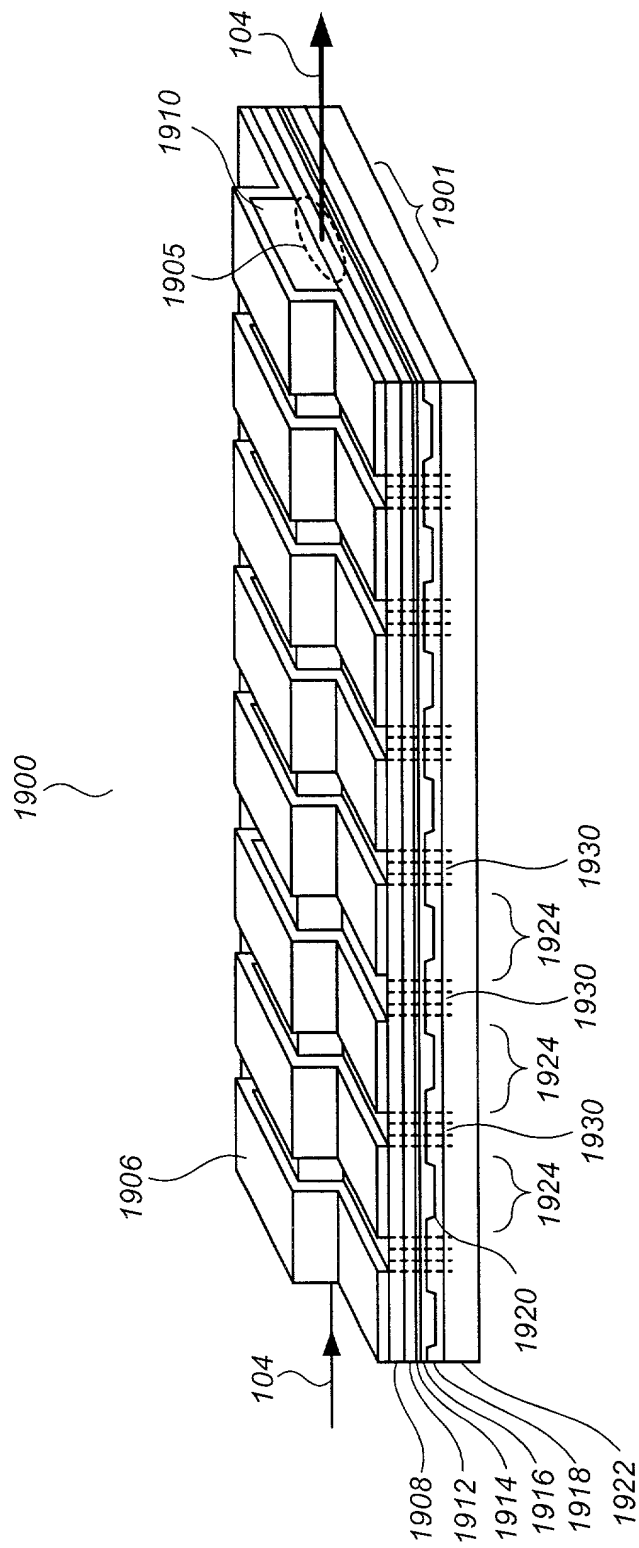


FIG. 19A

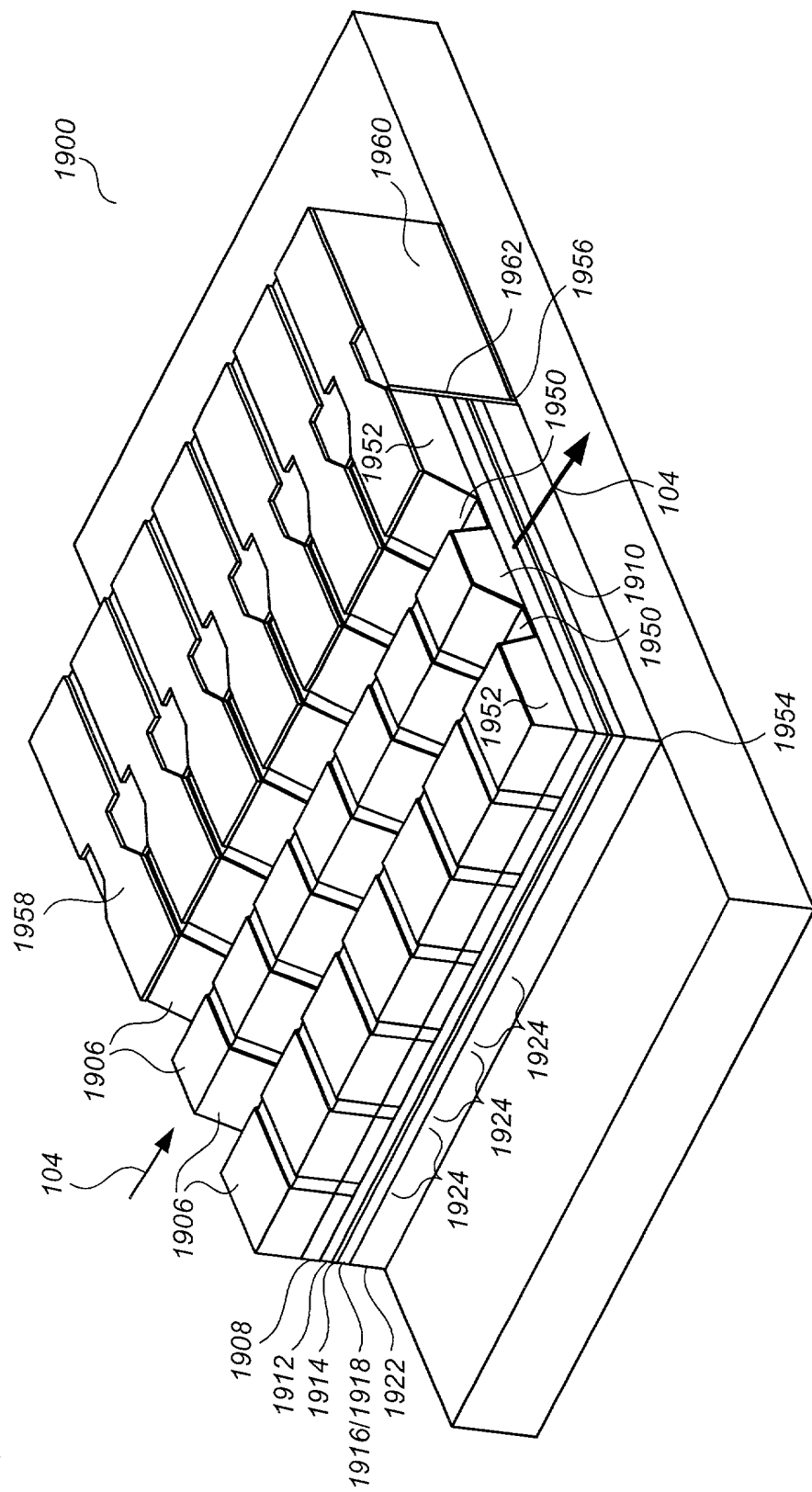


FIG. 19B

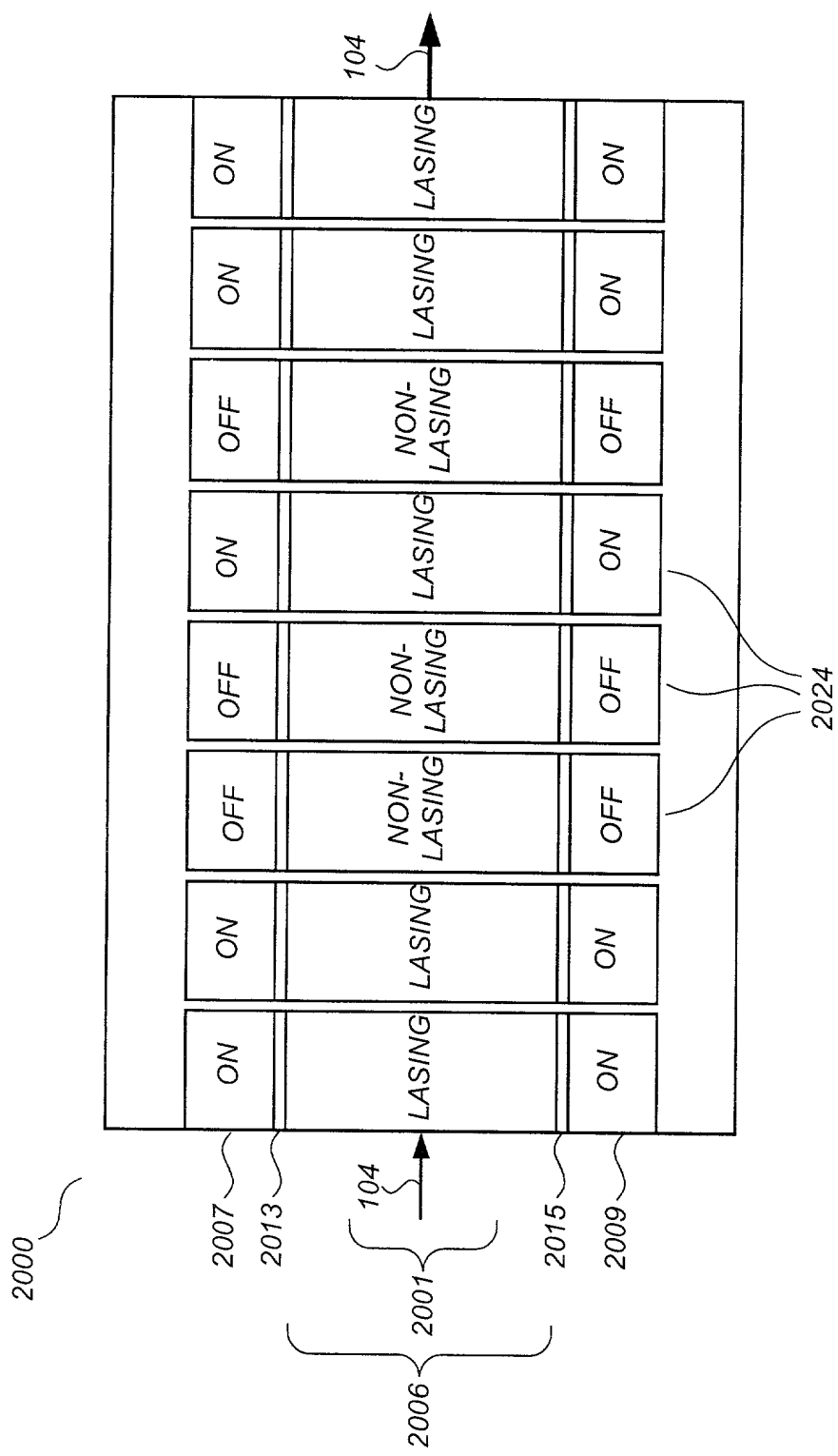


FIG. 20

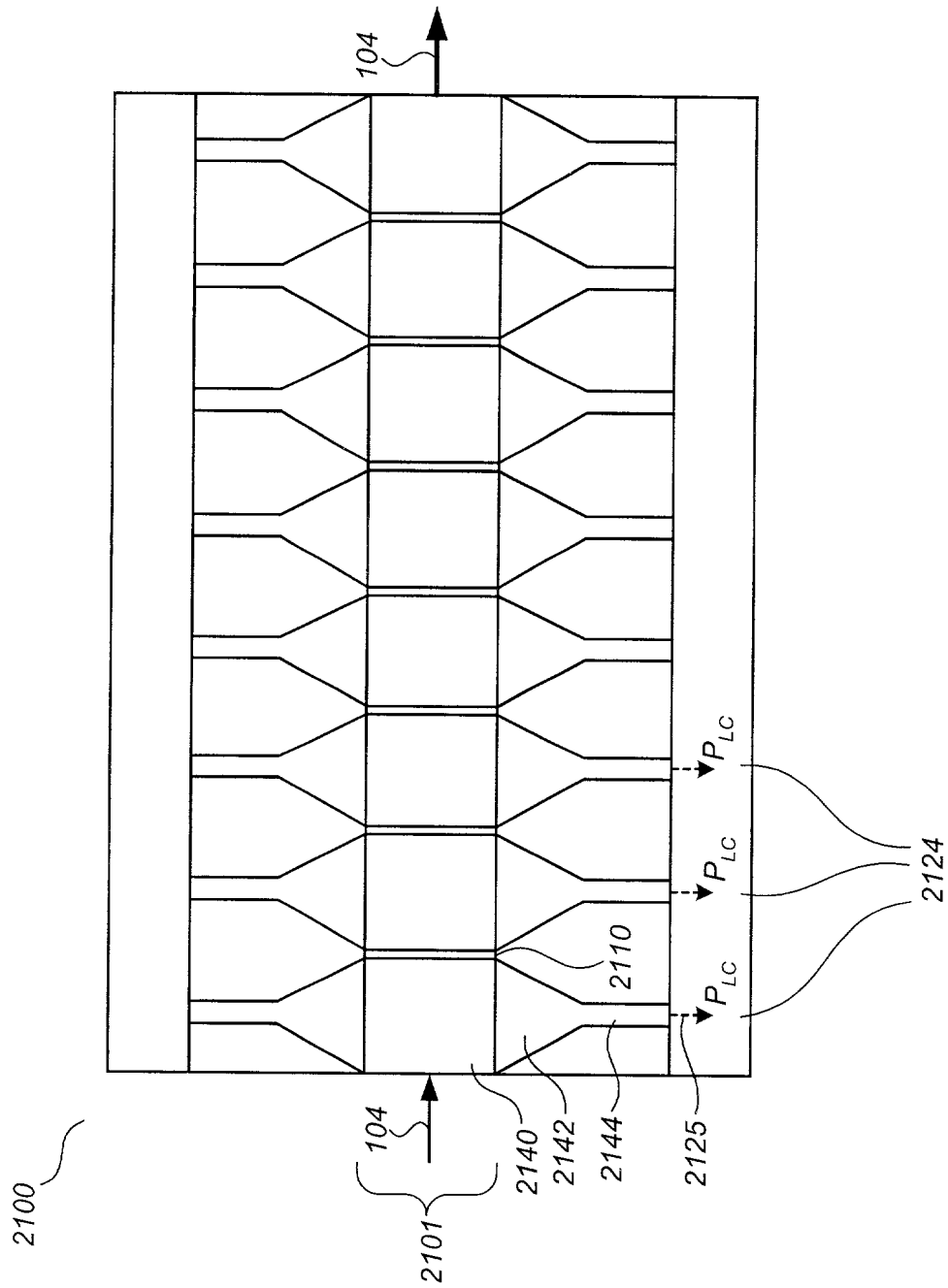


FIG. 21



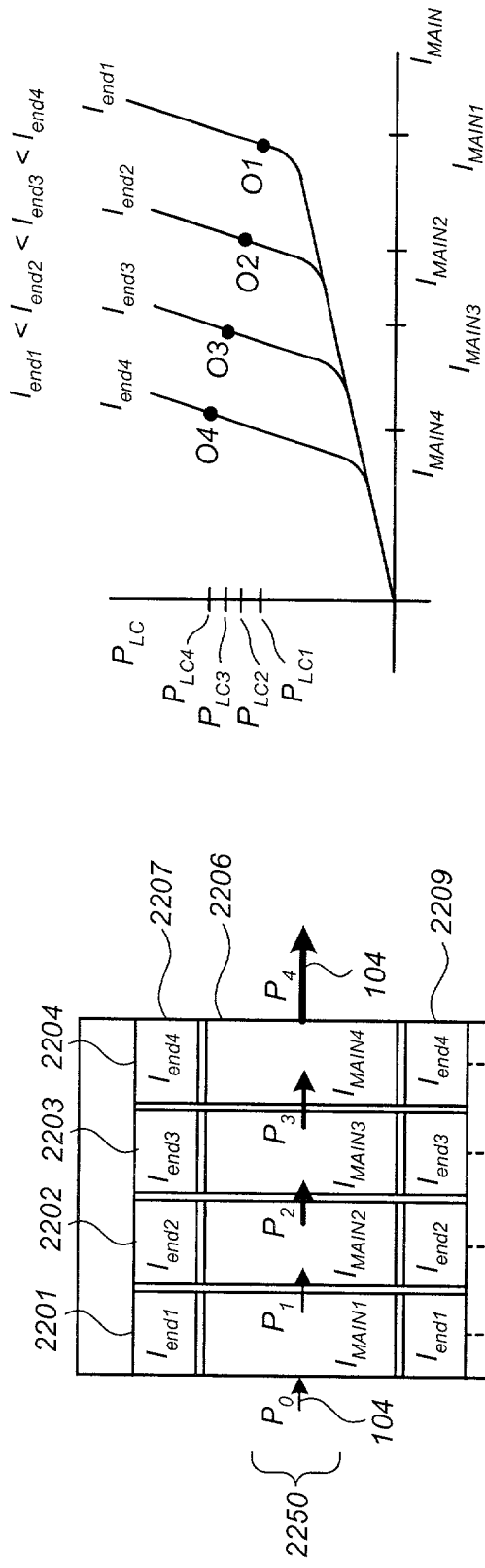


FIG. 23

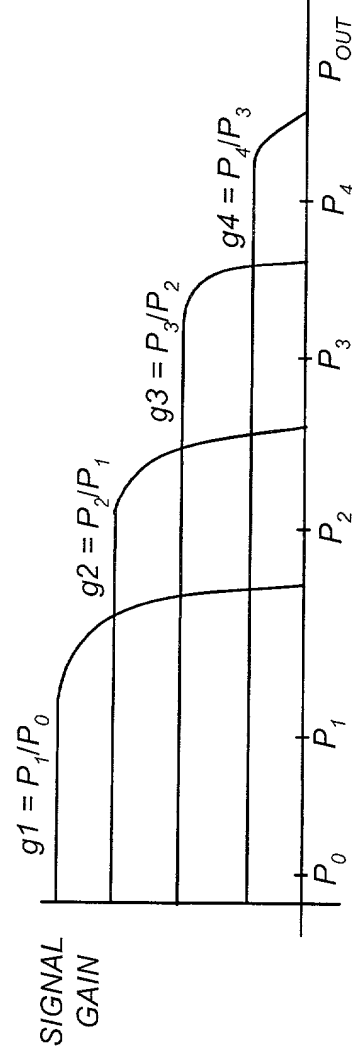
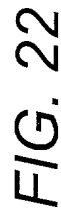


FIG. 24A

FIG. 24B is a graph showing the relationship between the signal gain and the input power for the system of FIG. 24A.

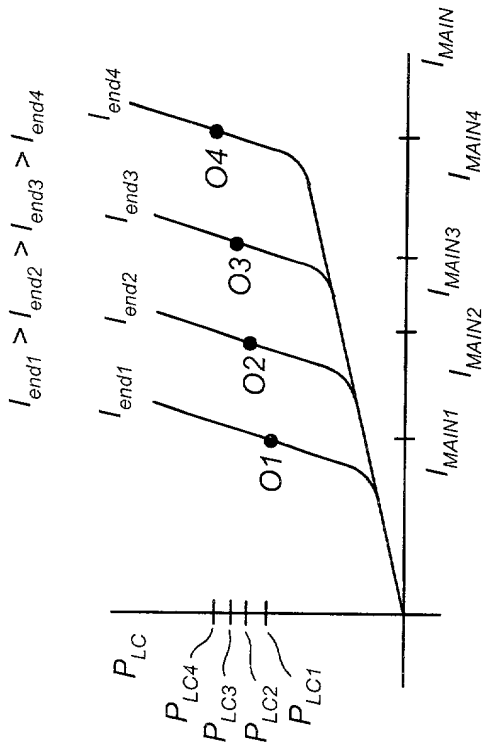


FIG. 24B

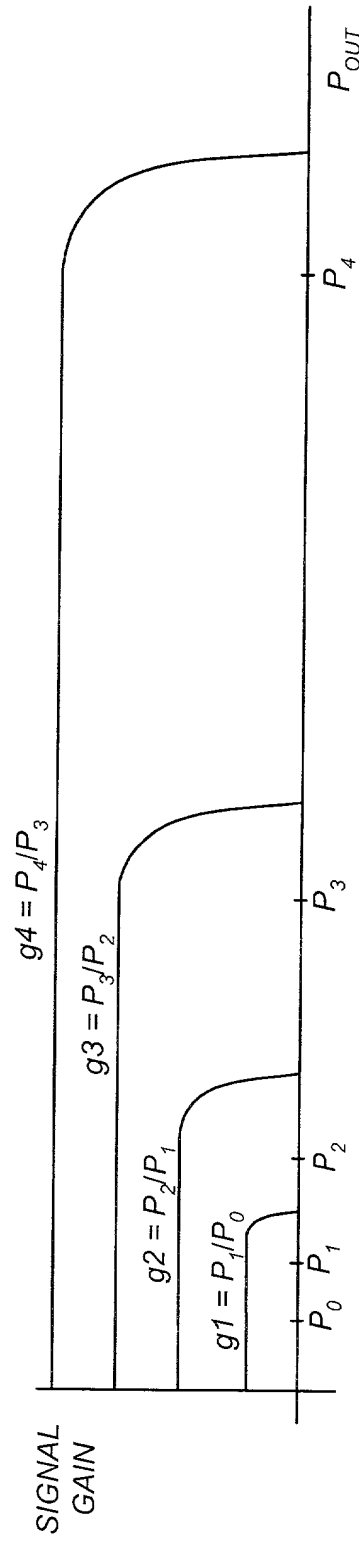


FIG. 24C

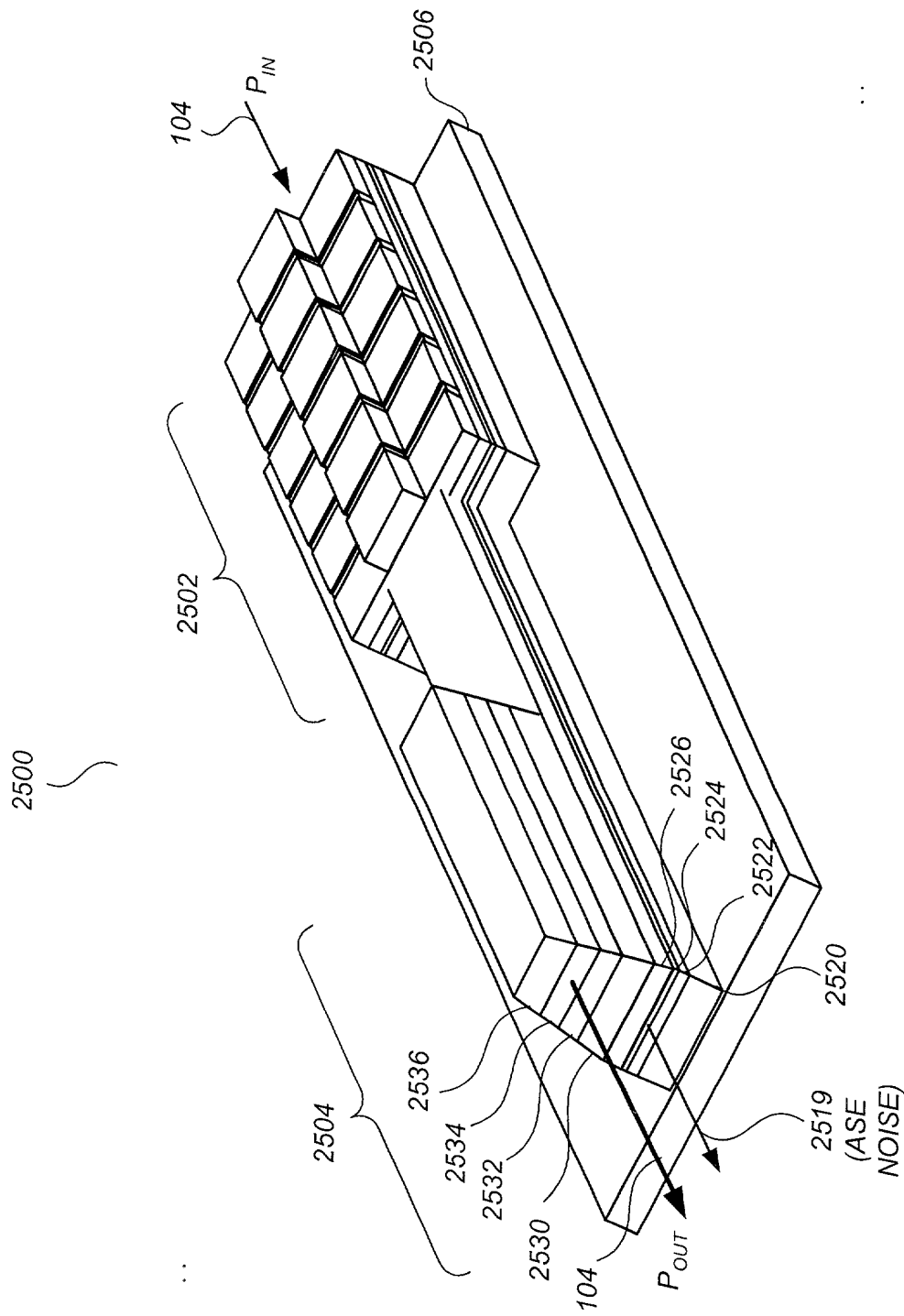


FIG. 25

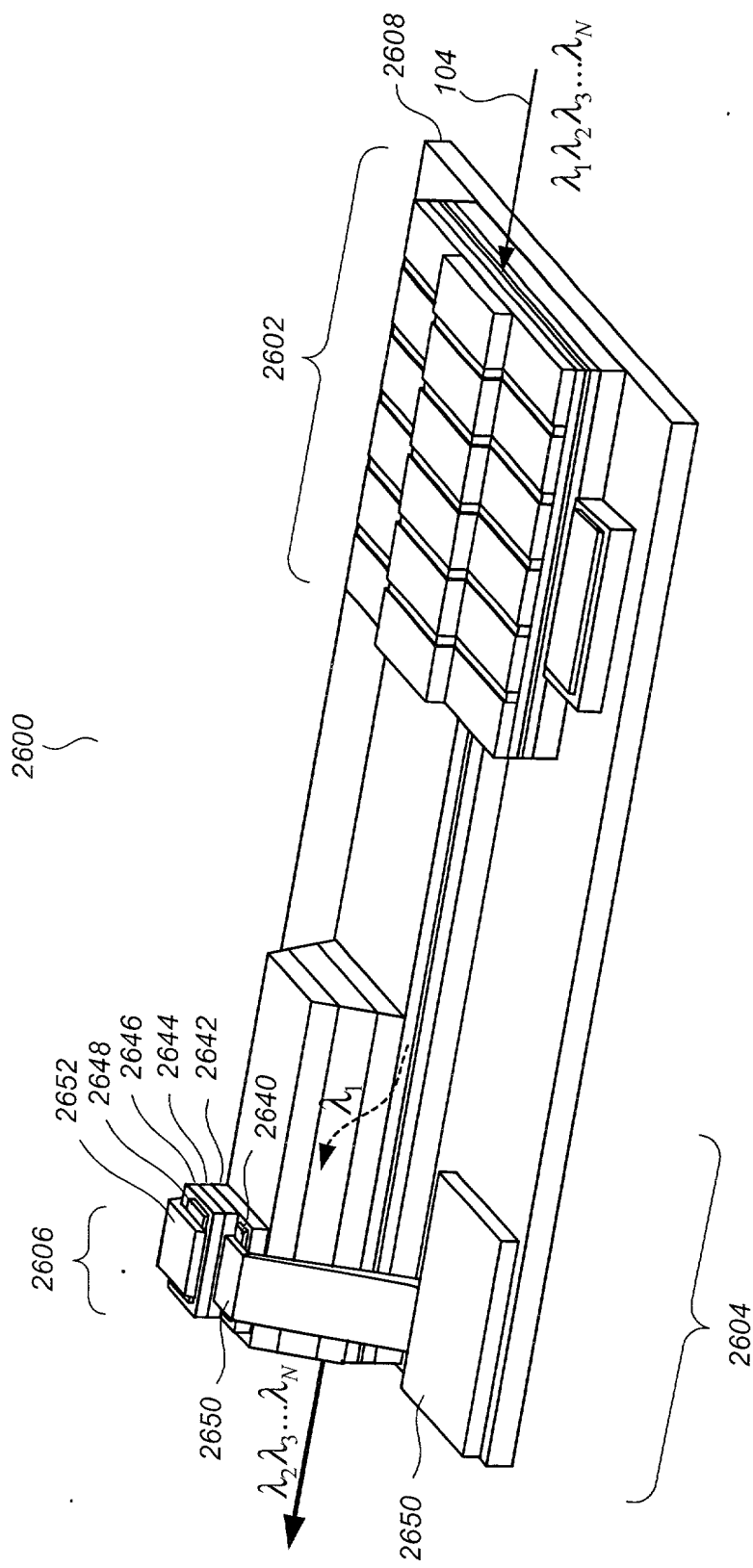


FIG. 26

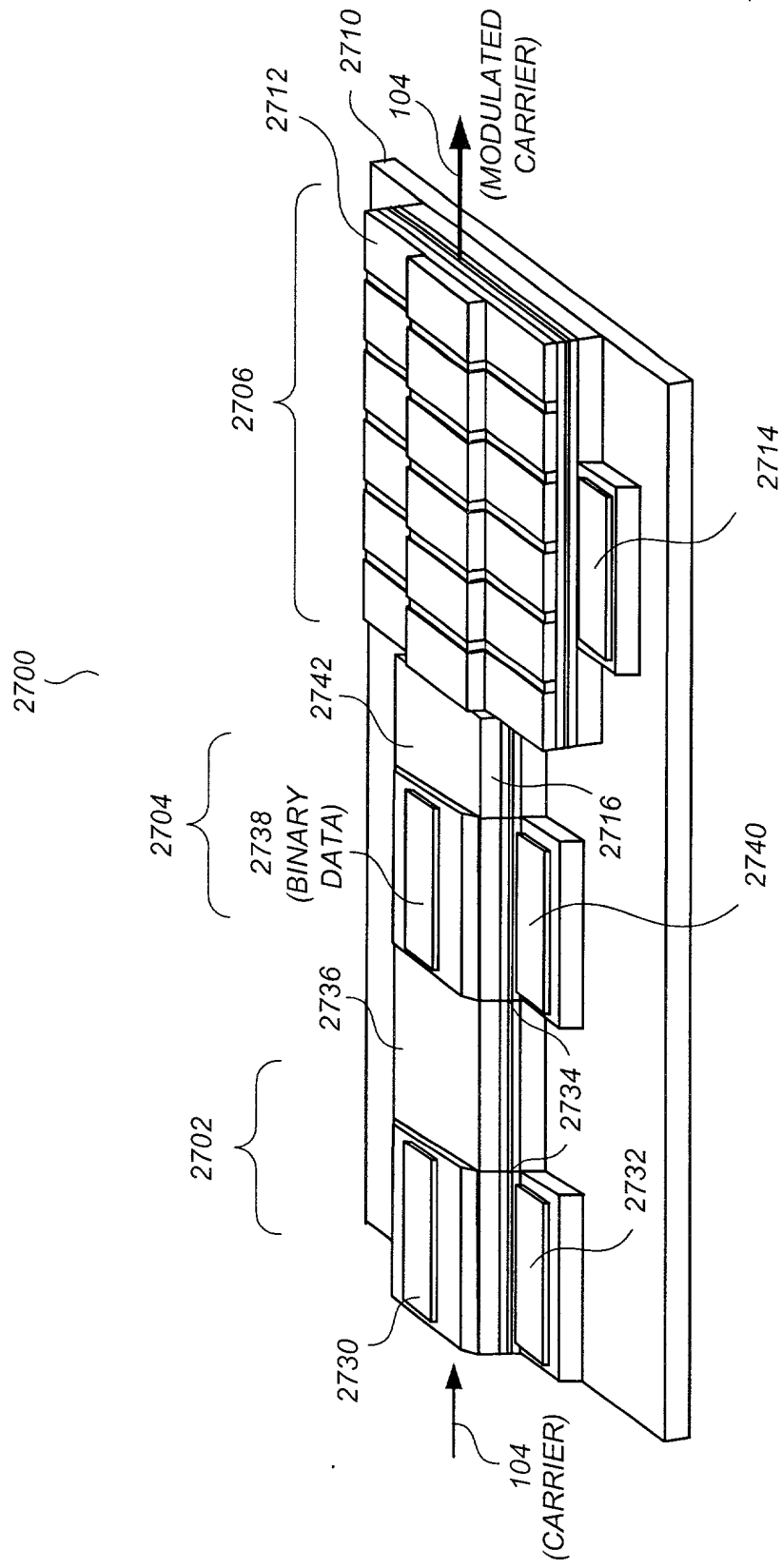


FIG. 27

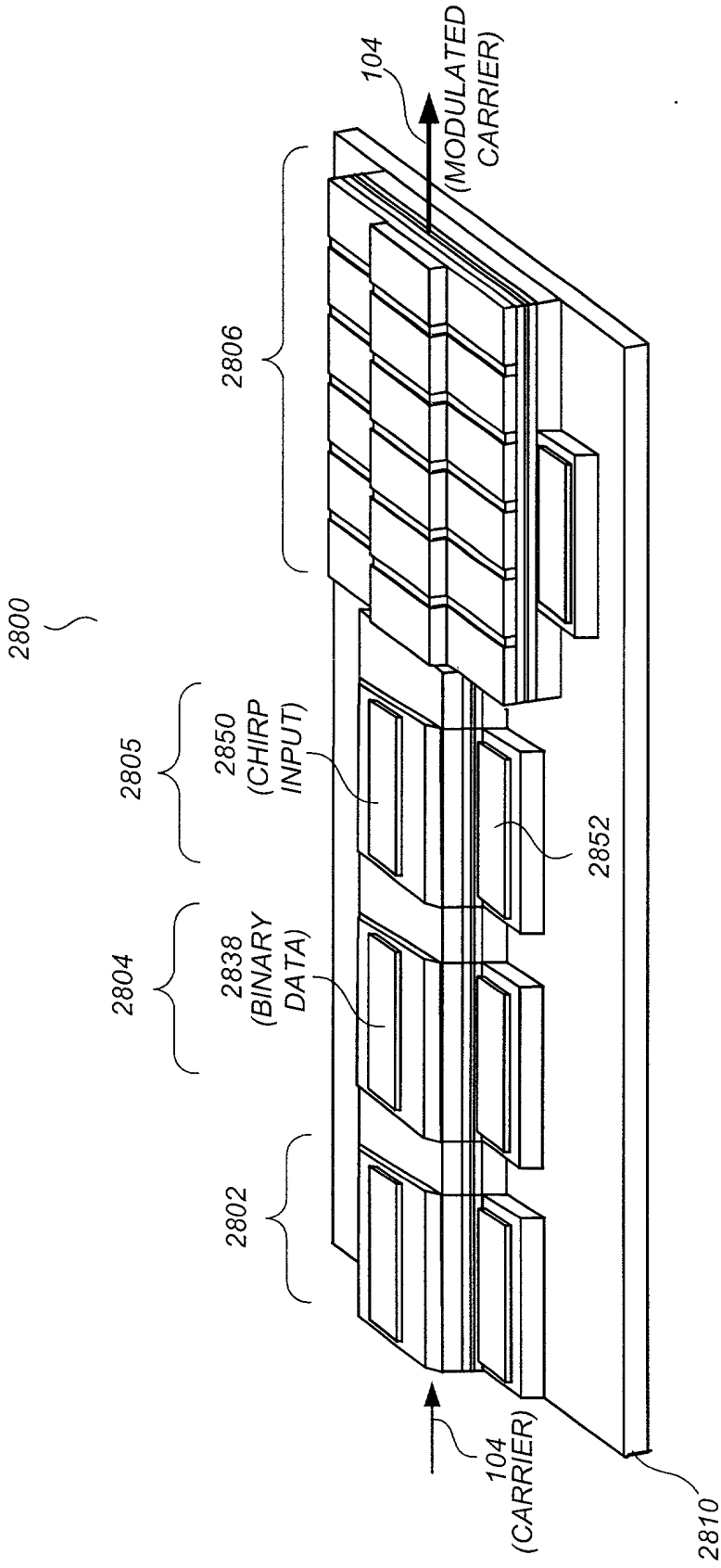


FIG. 28

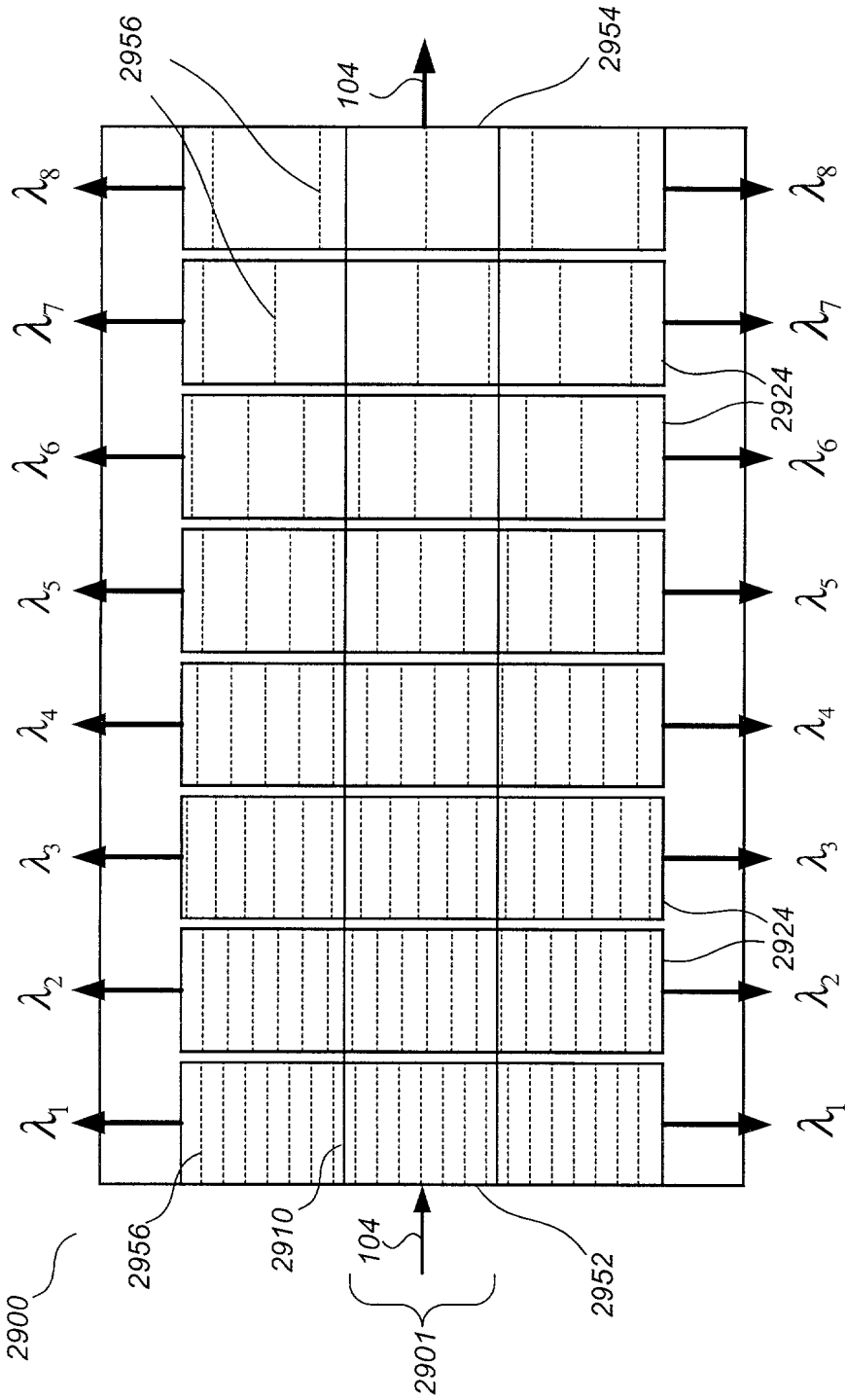


FIG. 29

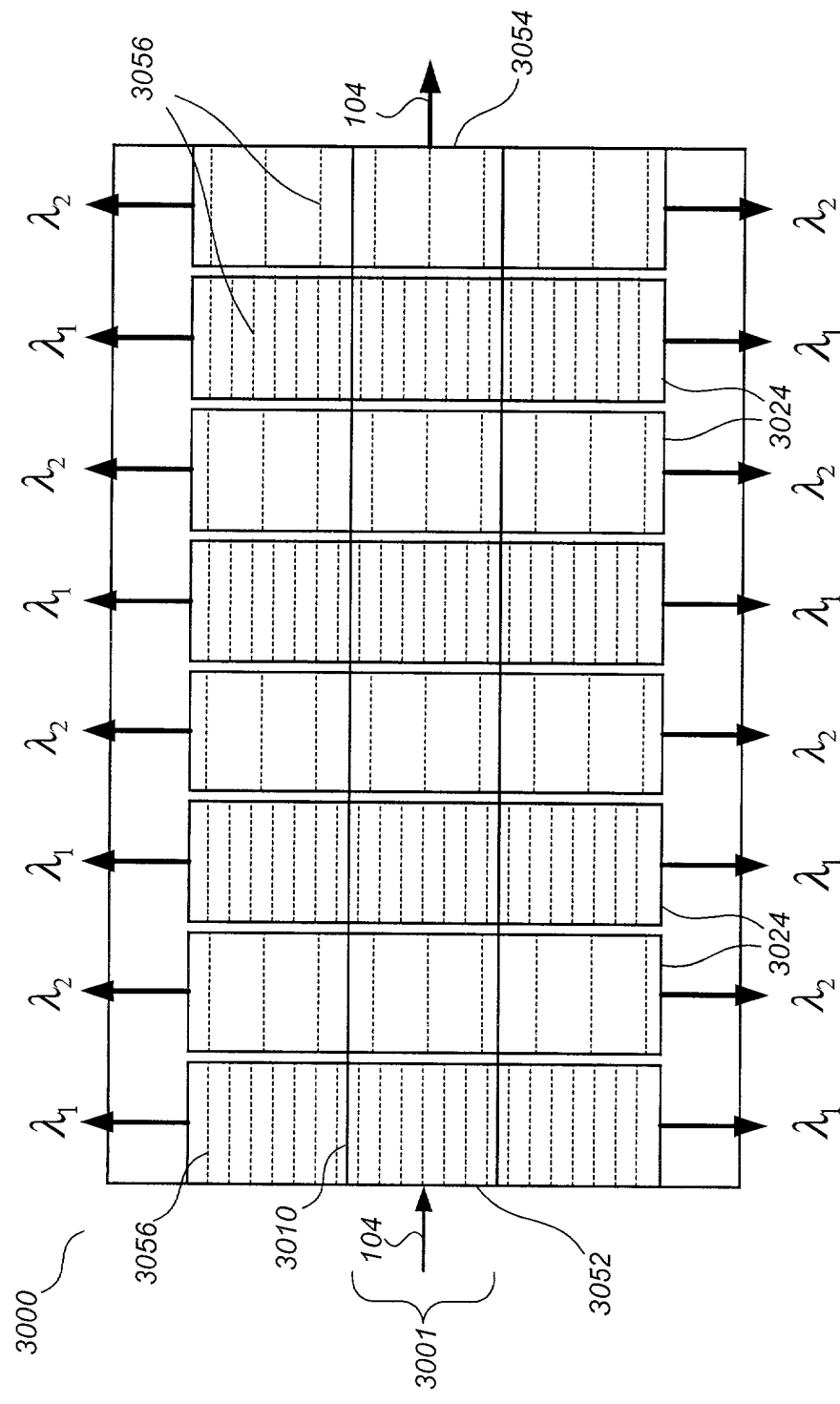


FIG. 30



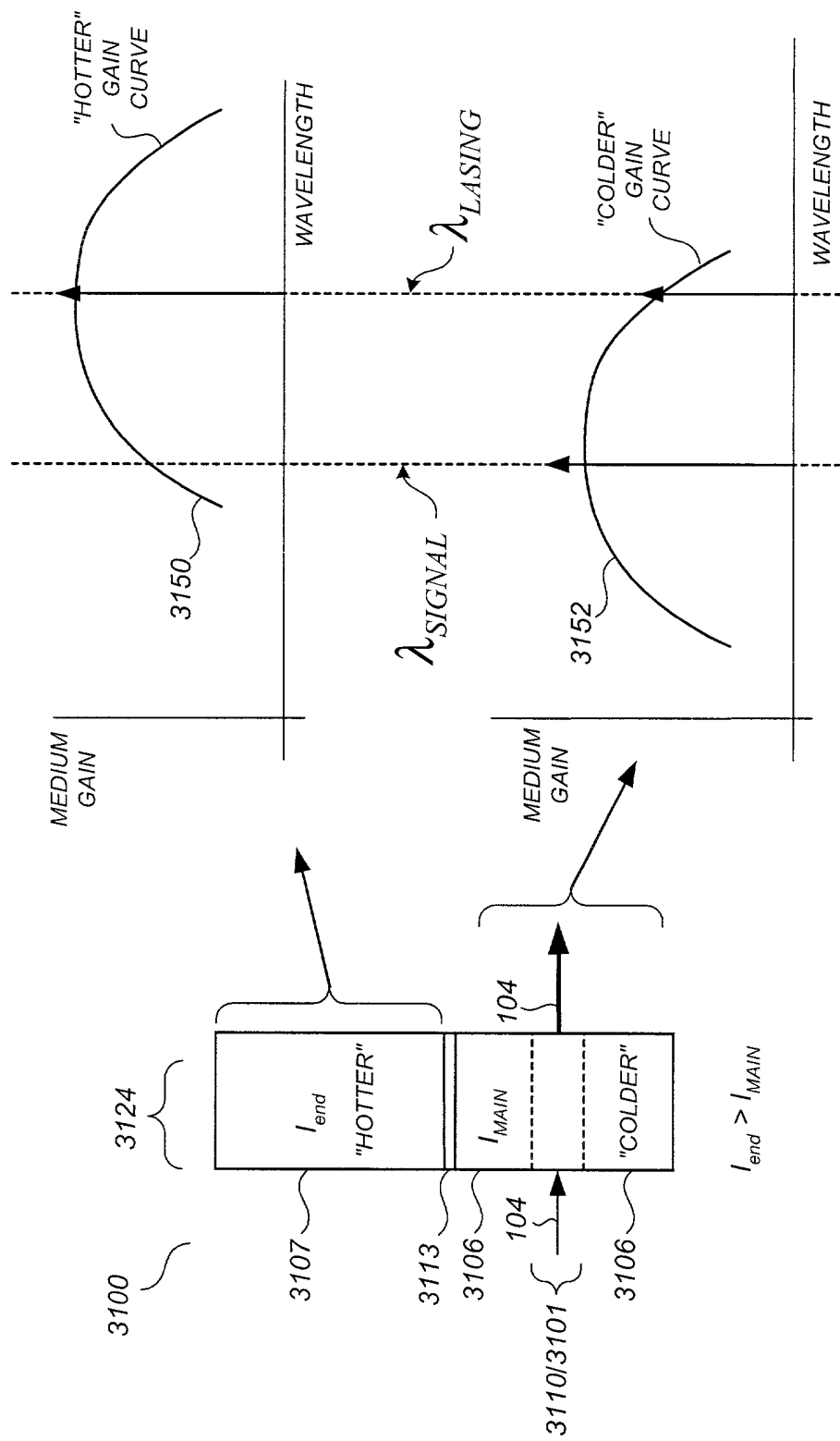


FIG. 31



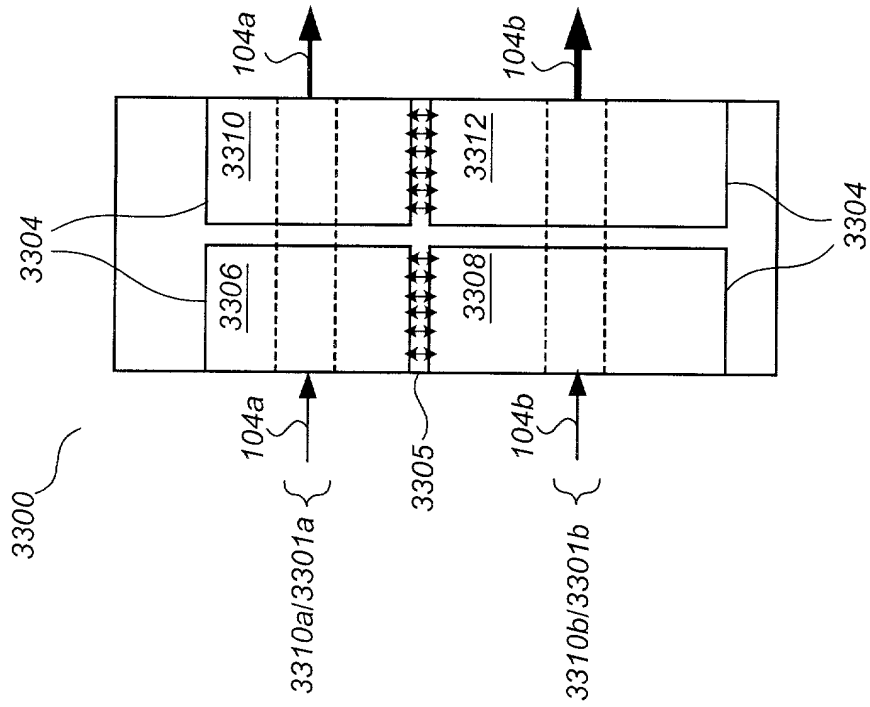


FIG. 33

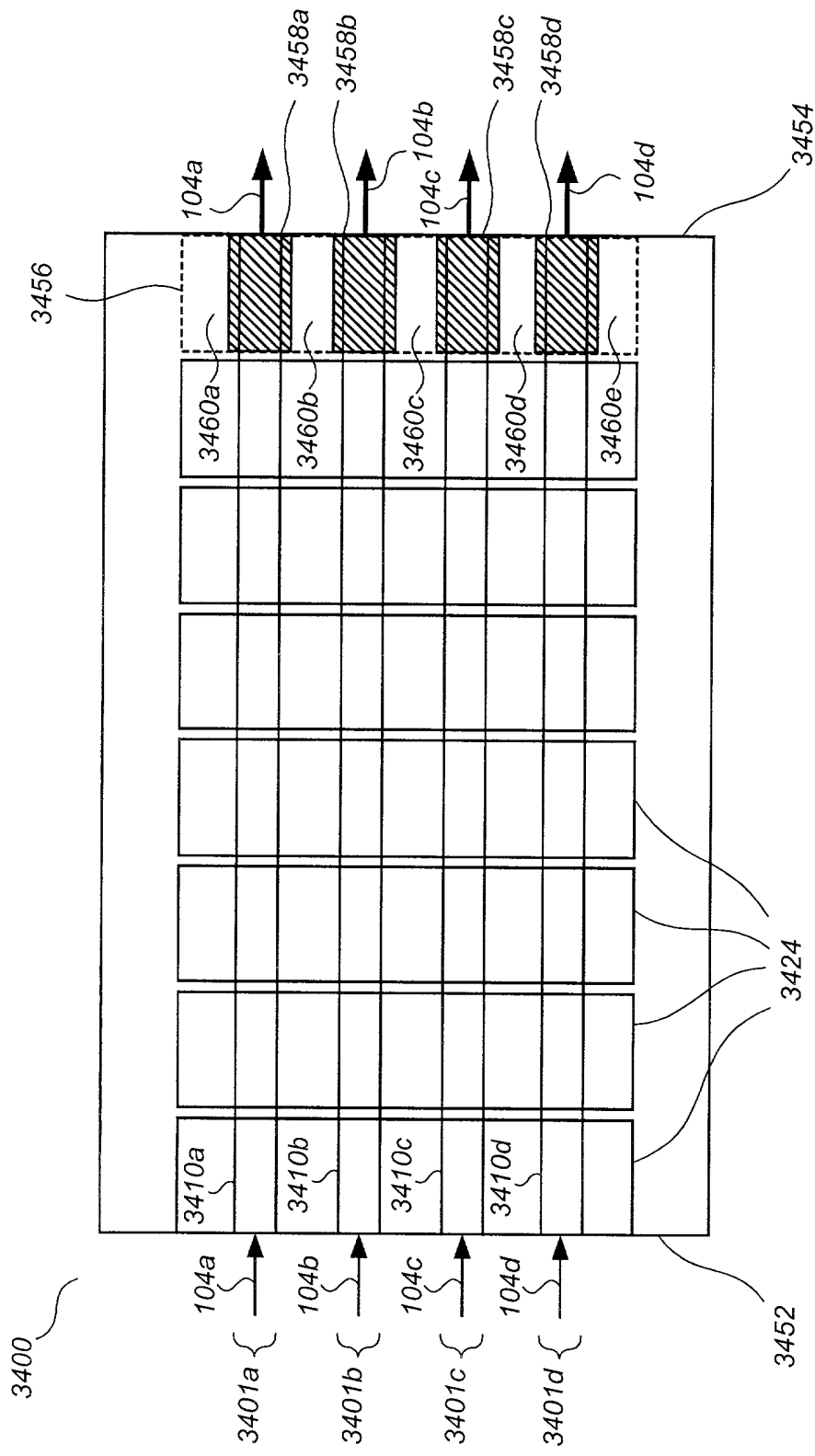


FIG. 34